

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>249114US2</b>		SERIAL NO. <b>10/779621</b> <b>NEW APPLICATION</b>	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Takashi OHSAWA</b>			
				FILING DATE <b>HEREWITH 2/18/04</b>		GROUP <b>2815</b>	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
<i>kw</i>	AA	6,621,725 B2	09/16/03	Takashi OHSAWA			
<i>kw</i>	AB	6,538,916 B2	03/25/03	Takashi OHSAWA			
<i>kw</i>	AC	6,617,651 B2	09/09/03	Takashi OHSAWA			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
<i>kw</i>	AO	2002-246571	08/30/02	Japan (with English Abstract)	<i>class sub class</i>		
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
<i>kw</i>	AW	Charles KUO, et al., "A Capacitorless Double-Gate DRAM Cell Design for High Density Applications", IEDM TECH. DIGEST, Dec. 2002, pgs. 843-846					
	AX						
	AY						
	AZ						<input type="checkbox"/> Additional References sheet(s) attached
Examiner <i>nc</i>					Date Considered <b>12/22/04</b>		
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							